

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

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**INFORMATION DISCLOSURE
STATEMENT**

Docket Number
10191/1639

Application Number
To Be Assigned

Filing Date
Herewith

Examiner
To Be Assigned

Art Unit
To Be Assigned

Invention Title
METHOD FOR ACTIVATING A JTAG INTERFACE OF
A MICROPROCESSOR OF A MICROCONTROLLER
UPON WHICH A JTAG INTERFACE IS
IMPLEMENTED, AND MICROCONTROLLER

Inventor(s)
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Assistant Commissioner
for Patents
Washington, D.C. 20231

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following reference to the attention of the Examiner. The reference is listed on the attached modified PTO form 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated on the modified PTO form 1449.

Dated: 1/11/01

By: [Signature]
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